

Workshop/Panel speaker:

Name: Cătălin Ciobanu
Job position: Associate Professor

Company: **Transilvania University of Brasov**

Job position: Senior Researcher

Company: **National Institute for Research and Development in Microtechnologies - IMT Bucharest**

e-mail: catalin.ciobanu@unitbv.ro
catalin.ciobanu@imt.ro

Title of the Presentation:
Lowering the Entry Barrier for Advanced Packaging in Education and Research

Short CV

Cătălin Ciobanu has a MSc and PhD from Delft University of Technology, The Netherlands. He is currently Associate Professor at Transilvania University of Brasov, Romania and Senior Researcher at the National Institute for Research and Development in Microtechnologies - IMT Bucharest, Romania. His research interests include RISC-V processors, embedded systems, high-performance computing, SIMD architectures, digital signal processing and reconfigurable hardware.

Abstract

The industry is moving towards advanced packaging and Systems in Package. Chiplets disaggregate large designs. There is a close interaction between Universities, EDA tools, industry and chiplets. We can lower the entry barrier for advanced packaging in education and research by embracing open-source.