



PDC Trainer: Paul CARPINE
Job Position: Account
Technology Manager

Company: Siemens EDA

e-mail:
paul.carpine@siemens.com

Title of the presentation: A PCB DFM Checklist Helps You Pass a Design Review

Short CV:

After graduating the “Politechnica” University of Timisoara (Romania), I started my career as SMT Process Engineer in Flex, more than 15 years ago. From 2009 I moved to Automotive Industry and worked for Forvia(Hella) as a NPI Engineer and later as a Prototyping Manager. Since 2016 I am living in Germany and developed my career in design for manufacturing and design for reliability, working for ZF and Marquardt Group. I joined Siemens EDA in 2022 bringing the practical experience in electronics manufacturing and design for manufacturing.

Professional Development Courses Outline:

Part 1: The Traditional PCB Release Process (20 min)

- Overview of checklist-based validation
- Common bottlenecks in release meetings

Part 2: Introduction to DFM Tools (30 min)

- What is DFM and benefits of early validation?
- How DFM tools work with fabricator profiles

Part 3: Modernizing the Checklist (30 min)

- Automating checks with DFM
- Key questions to include in a modern checklist
- Reducing manual measurements and errors

Part 4: Collaborative Review and Best Practices (30 min)

- Using DFM reports to guide discussions