



Keynote speaker:

Name: **MAREK LIS**

Job position: **Senior Analog Applications Engineer**

Company: **Texas Instrument, Inc.**

The University of Michigan

The University of Arizona

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Title of the Presentation:

A Long-term Stability of IC Parameters and the Temperature Acceleration Factor

Supports:

- Operational Amplifiers
- Voltage References
- Macro-models
- Long-term Stability
- Statistical Guarantee of Specs
- Conducts Analog Training Seminars

Marek Lis is a Senior Analog Applications Engineer on Precision Analog (PA) High Performance Linear team at Texas Instruments - Tucson Design Center. Prior to its acquisition by Texas Instruments in 2001, he had worked for 10 years at Burr-Brown Corporation as an analog IC design engineer introducing over twenty new products including JFET and CMOS op amps and LDO's. Afterwards, he held for eight years a design manager position in charge of development of series/shunt voltage references as well as instrumentation amplifiers and op amps. In current position as an application engineer, co-invented Green-Lis op amp macro-model-state-of-the-art *PSpice-based* operational amplifier model architecture optimized for Tina-TI; it is a behavioral simulation tool that meticulously simulates the performance of over twenty ac and dc parameters - it is a highly innovative model architecture that is unparalleled in the industry. His current focus is to provide customer support covering topics of operational amplifiers, voltage references as well as answer questions across TI product lines regarding long-term shift of various IC's parameters and statistical guarantee of various specifications over temperature. He also spends a significant amount of time conducting technical seminars all over the globe covering in-depth topics related to analog design and circuit applications.

He studied Electrical Engineering at the University of Michigan and University of Arizona.