





Constructive use of metal-semiconductor contact effects in thin-film transistors

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1. Creatively exploring design variables may lead to interesting solutions

- Electronic device physics
- Human-environment interaction
- Paper
- 2. Industry collaboration increases relevance for academic research
- 3. Talent is everywhere taking a chance pays off most of the time

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Wonderful things happen here

Surrey is 6th in *The Guardian* league table 2015 and 11th in *The Times* and *Sunday Times Good University Guide* 2015.

> Electronic Engineering









Applications of our research

- Surrey has pioneered the manufacture small scale satellites, Galileo project
- Space robotics and vehicles
- Propulsion systems and navigation
- Satellite Remote sensing & Disaster Monitoring
- Autonomy and Control Systems
- RemoveDEBRIS from space







What we do: Vision, Speech and Signal Processing



Applications of our research

- Computer Vision & Graphics
- Pattern Recognition
- 3D Video and Audio
- Machine Learning and AI
- Biometrics & security
- Digital Signal Processing
- Media content and streaming
- Medical Imaging





What we do: 5G Innovation Centre (5GIC)



The largest international research Centre of Excellence in new generation mobile technologies and Internet of Things (£70m+ investment); in healthcare & dementia care, data and cyber security, and autonomous vehicles





What we do: Advanced Technology Institute



Applications of our research

- All aspect of nanoscience and nanotechnology
- Graphene and new materials
- Functional Nanomaterials
- Energy production and storage
- Solar cells & batteries
- Printable & plastic electronics
- UK Facility for Ion Implantation









Advanced Technology Institute





Quantum technologies Photonics Spintronics

Ion implantation

Nanoelectronics Flexible and printed

RF and metrology

NPL South Hub





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UNIVERSITY OF

Microns

Virtually any material system

- Contact barrier (but not necessarily Schottky)
- Fully depleted semiconductor at the source
- Can be made alongside conventional TFTs







SGT: material systems















Schottky SGT vs FET

But...

Low saturation voltage Low output conductance (sat.)

Tolerance to short channel effects Tolerance to process variations

Improved bias stress stability

Lower ON-current than FETs

Low ON dynamic range

High temperature coefficient

SGT applications: analog



IEEE TRANSACTIONS ON ELECTRON DEVICES, VOL. 57, NO. 10, OCTOBER 2010



Intrinsic Gain in Self-Aligned Polysilicon Source-Gated Transistors

Radu A. Sporea, Member, IEEE, Michael J. Trainor, Nigel D. Young, John M. Shannon, and S. Ravi P. Silva



SGT applications: digital





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Source-gated transistors for order-of-magnitude performance improvements in thin-film digital circuits

R. A. Sporea, M. J. Trainor, N. D. Young, J. M. Shannon & S. R. P. Silva

Altmetric score (what's this?)

Tweeted by 4 On 3 Facebook pages Picked up by 3 news outlets

This Altmetric score means that the article is:

- in the 94 percentile (ranked 4,737th) of the 82,649 tracked articles of a similar age in all journals
- in the 92 percentile (ranked 54th) of the 719 tracked articles of a similar age in Scientific Reports

26

Analog: material quality – polycrystalline





Analog: barrier screening





IEEE T-ED, 59, 2180, (2012)





Supply voltage

Analog: geometrical variations







Schottky SGT vs FET

But...

Low saturation voltage Low output conductance (sat.)

Tolerance to short channel effects Tolerance to process variations

Improved bias stress stability

Lower ON-current than FETs

Low ON dynamic range

High temperature coefficient

Heterostructure and bulk barrier SGTs





Current injection: modes of operation





Current injection: modes of operation



Temperature dependence of drain current on source length



Valletta, A., Mariucci, L., Rapisarda, M. & Fortunato, G., JAP 114, 064501 (2013).

Shannon, J. M. et al., *IEEE T-ED* **60**, 2444–2449 (2013).

Friday, 22 December 2017

ITC 2016, Hsinchu

Current injection: modes of operation



Temperature dependence of drain current on source length



Shannon, J. M. et al., *IEEE T-ED* **60**, 2444–2449 (2013).

Friday, 22 December 2017

Analog: barrier screening

Simple field relief structures reduce current dependence on temperature







Analog: barrier screening



Simple field relief structures reduce current dependence on temperature



Friday, 22 December 2017

Summer placements





Summer placements



Science & Environment

School student's cool plastic electronics

By Yasmin Ali Science reporter, Bradford

C 12 September 2015 Science & Environment

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Source gated transistors, like these made on glass at Surrey University, could help deliver flexible electronics





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"We interact with the floor every single second of our existence. Why don't get any data out of it?"







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Augmented paper



EPSRC Reference:	EP/P02579X/1						
Title:	Next generation paper						
Principal Investigator:	Frohlich, Professor D						
Other Investigators	Revill, Dr G	Bober, Professor M		r M S	Scarles, Professor C		
Other Investigators.	Sporea, Dr RA	Brown, Professor A W		r A W			
Researcher Co- Investigators:							
Project Partners:	Bradt Travel Guides	Hewlett Packard		III	Ifolor Oy		
	Librios	Novacentrix		C	Otava Publishing Company		
	Private Address	The Emirates Group (UK)					
Department:	Digital World Research Centre						
Organisation:	University of Surrey						
Scheme:	Standard Research						
Starts:	01 October 2017	Ends:	31 March	2020	Value (£):	953,078	
EPSRC Research Topic Classifications:	Computer Graphics & Visual.		Displays				
	Human-Computer Interactions		Recreation/Tourism Geography				
EPSRC Industrial Sector Classifications:	Information Technologies						



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Thank you for your attention!

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